



CLASS TIME TABLE

Class Room-E101

Semester : 3

A.Y.2024-25

Class: SE (A)

w.e.f. - 01/07/2024

Time	Day	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday	ClassRoom Location
08:30am to 09:30am		DM(DBS)(E101)	DELD(SAA)(E101)	DM(DBS)(E101)	OOP(VDR)(E101)	A1: OOP(VDR)(C104) A2: CGL(MVK)(C106) A3: DEL(SAA)(C103) A4: BCS(PD)(C108)		E101
09:30am to 10:30am		FDS(DJB)(E101)	DM(DBS)(E101)	DELD(SAA)(E101)	CG(MVK)(E101)			
10:30am to 10:45am								
10:45am to 11:45pm		A1: DSL(YPW)(C110) A2: OOP(VDR)(C104) A3: DSL(DBS)(C111) A4: CGL(MVK)(C106)	A1: CGL(MVK)(C106) A2: DSL(DJB)(C110) A3: BCS (MD)(C108) A4: DSL(DBS)(C111)	A1: DEL(PVD)(C103) A2: DSL(DJB)(C111) A3: DSL(YPW)(C110) A4: OOP(VDR)(C104)	A1: DSL(DBS)(C111) A2: DEL(SAA)(C103) A3: CGL(MVK)(C106) A4: DSL(DJB)(C110)	A1: BCS(PD)(C108) A2: BCS (MD)(C110) A3: OOP(VDR)(C104) A4: DEL(PVD)(C103)		
11:45pm to 12:45pm								
12:45pm to 01:30pm								
01:30pm to 2:30pm		OOP(VDR)(E101)	FDS(DJB)(E101)	CG(MVK)(E101)	DELD(SAA)(E101)			E101
02:30pm to 3:30pm		CG(MVK)(E101)	OOP(VDR)(E101)	FDS(DJB)(E101)	HSS-TUT(VDR)(E101)			

SUBJECT	FACULTY NAME	Load		Practicals / Tutorial			
		Lectures	Practicals/ Tutorial	Batch A1	Batch A2	Batch A3	Batch A4
Discrete Mathematics (DM)	Mr. D.B.Satre	3	-	-	-	-	-
Fundamentals Of Data Structures (FDS)	Mrs. D. J. Bonde	3	-	-	-	-	-
Object Oriented Programming (OOP)	Mr. V. D. Rewaskar	3	-	-	-	-	-
Computer Graphics(CG)	Mrs. M.V. Kadam	3	-	-	-	-	-
Digital Electronics & logic Design (DELD)	Mr. S. A. Agrawal	3	-	-	-	-	-
Data Structures Laboratory (DSL)	Mrs. D. J. Bonde Mr. D.B.Satre	-	16	DBS YPW	DBS YPW	DBS YPW	DBS YPW
OOP and Computer Graphics Laboratory (OOPCGL)	Mr. V. D. Rewaskar Mrs. M.V. Kadam	-	16	VDR MVK	VDR MVK	VDR MVK	VDR MVK
Digital Electronics Laboratory (DEL)	Mr. S. A. Agrawal Ms. P. V. Deshmukh	-	8	PVD	SAA	SAA	PVD
Business Communication Skills (BCS)	Dr. P. Dhamdhare Dr. M. Dangore	-	8	PD	MD	MD	PD
Humanity and Social Science (HSS)	Mr. V. D. Rewaskar	-	4	VDR	VDR	VDR	VDR

Co-ordinator
Time Table

HOD
Dept. of Computer Engg.