



Marathwada Mitra Mandal's
Institute Of Technology, Lohgaon Pune - 47
Department of Computer Engineering



CLASS TIME TABLE

Class Room-E104

Semester : 3

A.Y.2024-25

Class: SE (B)

w.e.f. - 01/07/2024

Time	Day	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday	ClassRoom Location
08:30am to 09:30am		DM(MD)(E104)	CG(VVC)(E104)	OOP(YBD)(E104)	DELD(SAA)(E104)	B1: OOP(YBD)(C110) B2: CGL(VVC)(C105)		E104
09:30am to 10:30am		CG(VVC)(E104)	DM(MD)(E104)	FDS(DJB)(E104)	FDS(DJB)(E104)	B3: BCS(SAK)(C102) B4: BCS(DJB) (C101)		
10:30am to 10:45am								
10:45am to 11:45pm		DELD(SAA)(E104)	OOP(YBD)(E104)	DM(MD)(E104)	OOP(YBD)(E104)			E104
11:45pm to 12:45pm		FDS(DJB)(E104)	DELD(SAA)(E104)	CG(VVC)(E104)	HSS-TUT(VVC)(E104)			
12:45pm to 01:30pm								
01:30pm to 2:30pm		B1: DSL(DJB)(C110) B2: OOP(YBD)(C104) B3: DEL(SAA)(C103) B4: DSL(DBS)(C111)	B1: DEL(SAA)(C103) B2: DSL(YPW)(C110) B3: DSL(DBS)(C111) B4: CGL(VVC)(C106)	B1: CGL(VVC)(C106) B2: DEL(SAA)(C103) B3: DSL(DBS)(C110) B4: OOP(YBD)(C104)	B1: DSL(DBS)(C111) B2: BCS (VVC)(C108) B3: OOP(YBD)(C104) B4: DSL(DJB)(C110)	B1: BCS(DJB)(C108) B2: DSL(DBS)(C111) B3: CGL(VVC)(C106) B4: DEL(SAA)(C103)		
02:30pm to 3:30pm								

THEORY	FACULTY NAME	Load		Practicals / Tutotial			
		Lectures	Practicals/ Tutorial	Batch B1	Batch B2	Batch B3	Batch B4
Discrete Mathematics (DM)	Dr. M. Dangore	3	-	-	-	-	-
Fundamentals Of Data Structures (FDS)	Mrs. D. J. Bonde	3	-	-	-	-	-
Object Oriented Programming (OOP)	Mr. Y. B. Dongre	3	-	-	-	-	-
Computer Graphics (CG)	Mr. V. V. Chavan	3	-	-	-	-	-
Digital Electronics & logic Design (DELD)	Mr. S. A. Agrawal	3	-	-	-	-	-
Data Structures Laboratory (DSL)	Mrs. D. J. Bonde Mr. D.B.Satre	-	16	DJB DBS	DJB DBS	DJB DBS	DJB DBS
OOP and Computer Graphics Laboratory (OOPCGL)	Mr. Y. B. Dongre Mr. V. V. Chavan	-	16	YBD VVC	YBD VVC	YBD VVC	YBD VVC
Digital Electronics Laboratory (DEL)	Mr. S. A. Agrawal	-	8	SAA	SAA	SAA	SAA
Business Communication Skills (BCS)	Mrs. D. J. Bonde Mr. V. V. Chavan Dr. S. A. Karad	-	8	DJB	VVC	SAK	DJB
Humanity and Social Science (HSS)	Mr. V. V. Chavan	-	4	VVC	VVC	VVC	VVC

Co-ordinator
Time Table

HOD
Dept. of Computer Engg.